



IFR

PATENT
Docket No. Intel/17479

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant(s):) I hereby certify that this paper and the
) documents referred to as enclosed
 Kiran R. DESAI) therewith are being deposited with the
) United States Postal Service as first class
 U.S. Serial No.: 10/630,164) mail, postage prepaid, in an envelope
) addressed to Commissioner for Patents,
 For: "Methods and Apparatus for) P.O. Box 1450, Alexandria, Virginia
 Maintaining Cache Coherency") 22313-1450 on this date:
)
 Filed: July 30, 2003)
) **August 30, 2005**
 Assignee: Intel Corporation)
)
 Group Art Unit: 2186)
)
 Examiner: Not Yet Assigned) *James A. Flight*
) James A. Flight
) Attorney for Applicant(s)
) Registration No. 37,622

STATUS LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Kindly advise when an Office action can be expected in the above-referenced matter.

Respectfully submitted,

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August 30, 2005

By:

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